



Docket No.: L&L-10282

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Wolfgang Rösner et al.  
Applic. No. : 10/047,013  
Filed : January 16, 2002  
Title : Method for Producing a Vertical Semiconductor  
Transistor Component and Vertical Semiconductor  
Transistor Component

**ASSOCIATE POWER OF ATTORNEY**

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

Please recognize MARKUS NOLFF (Reg. No. 37,006) as my associate in the matter in the above-identified application, with full powers. Please continue addressing all communications to the following address:

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Respectfully submitted,

  
For Applicants

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Date: March 21, 2002

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